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## POWER OF ATTORNEY CORRESPONDENCE ADDRESS **INDICATION FORM**

the Paperwork Reduction Act of 1995, no persons are red POWER OF ATTORNEY and RRESPONDENCE ADDRESS INDICATION FORM	Application Number	10/700,791		
	Filing Date	November 03, 2003		
	First Named Inventor	Allen III, Ernest		
	Title	Method for Testing IDD at Multiple		
	Art Unit	2829		
	Examiner Name	Hallington, Jermele M.		
	Attorney Docket Number	03-0724		

l hereby revoke a	I hereby revoke all previous powers of attorney given in the above-identified application.					
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Trademark Office con	nected therewith.				<u> </u>	
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Applicant/Inventor.						
Assignee of record of the entire interest. See 37 CFR 3.71.						
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)						
SIGNATURE of Applicant or Assignee of Record						
Signature	borne				Date	26JAN06
Name	Timothy R. Croll				elephone	(408) 433-7625
Title and Company Senior Corporate Counsel, IP, LSI Logic Corporation						
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.						
*Total of 1 forms are submitted.						

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer. U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/96 (12-05)
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STATEMENT UNDER 37 CFR 3.73(b)					
Applicant/Patent Owns	er: LSI Logic Corporation				
Application No./Patent	No./Control No.: 10/700,791	Filed/Issue Date: November	03.2003		
Entitled: Method for Te	sting IDD at Multiple Voltages	·			
LSI Logic Corporation		_ a Corporation			
(N	ame of Assignee)	(Type of Assignee: corporation, partne	rship, university, government agency, etc.)		
	the entire right, title, and interest, or				
2. an assignee of leading (The extent (by	ess than the entire right, title and interes percentage) of its ownership interest is_	t %}			
in the patent application	n/patent identified above by virtue of eit	her:			
A. An assignment fin the United State original assignm	rom the inventor(s) of the patent applicates Patent and Trademark Office at Ree ent is attached.	tion/patent identified above. The 014667 Frame 0317	e assignment was recorded, or a true copy of the		
B. A chain of title f	rom the inventor(s), of the patent applica	tion/patent identified above, to	the current assignee as follows:		
1. From: The docu Reel	Tument was recorded in the United States, Frame	o: Patent and Trademark Office a _, or for which a copy thereof is	attached.		
2. From:		0:			
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Additional do	ocuments in the chain of title are listed or	n a supplemental sheet.			
assignee was, or co	FR 3.73(b)(1)(i), the documentary evidencurrently is being, submitted for receive copy (i.e., a true copy of the original accordance with 37 CFR Part 3, to record	ordation pursuant to 37 CFR ( ssignment document(s)) must b	3.11. be submitted to Assignment		
The undersigned (who	pse title is supplied below) is authorized	to act on behalf of the assignee	ZC JANO6		
	Signature		Date		
	Timothy R. Croll		(408) 433-7625		
	Printed or Typed Name		Telephone Number		
	Senior Corporate Counsel, IP Title				

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